

FIG. 1A

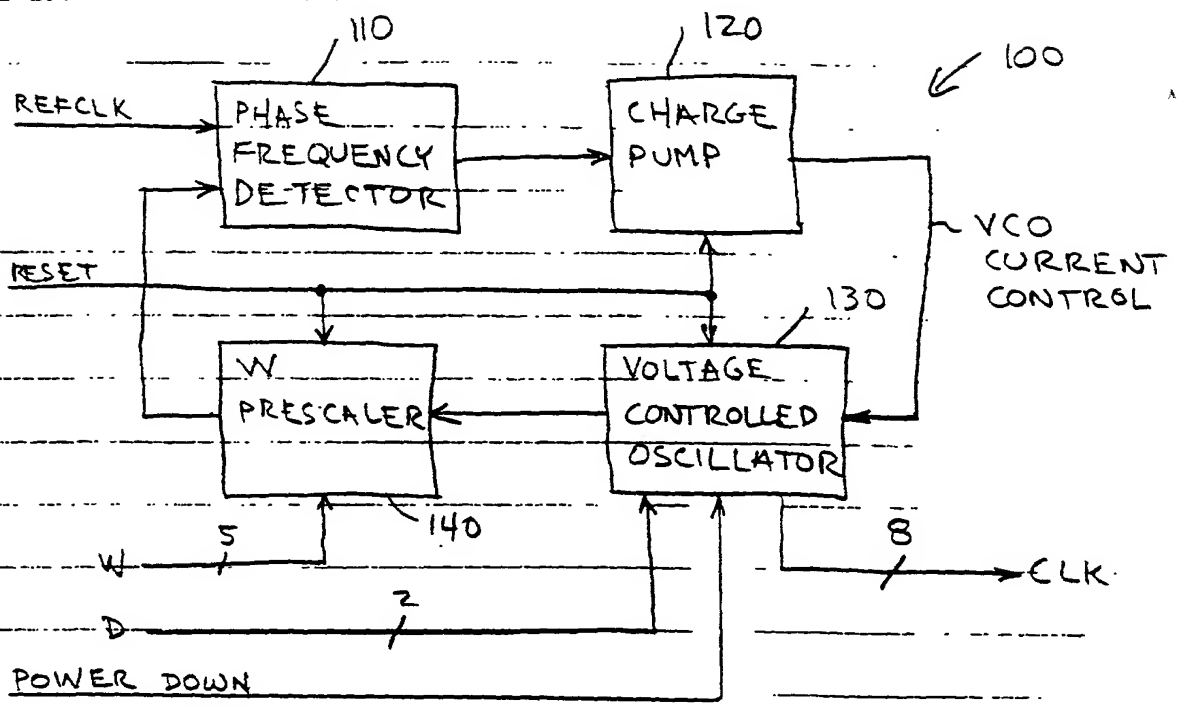


FIG. 2

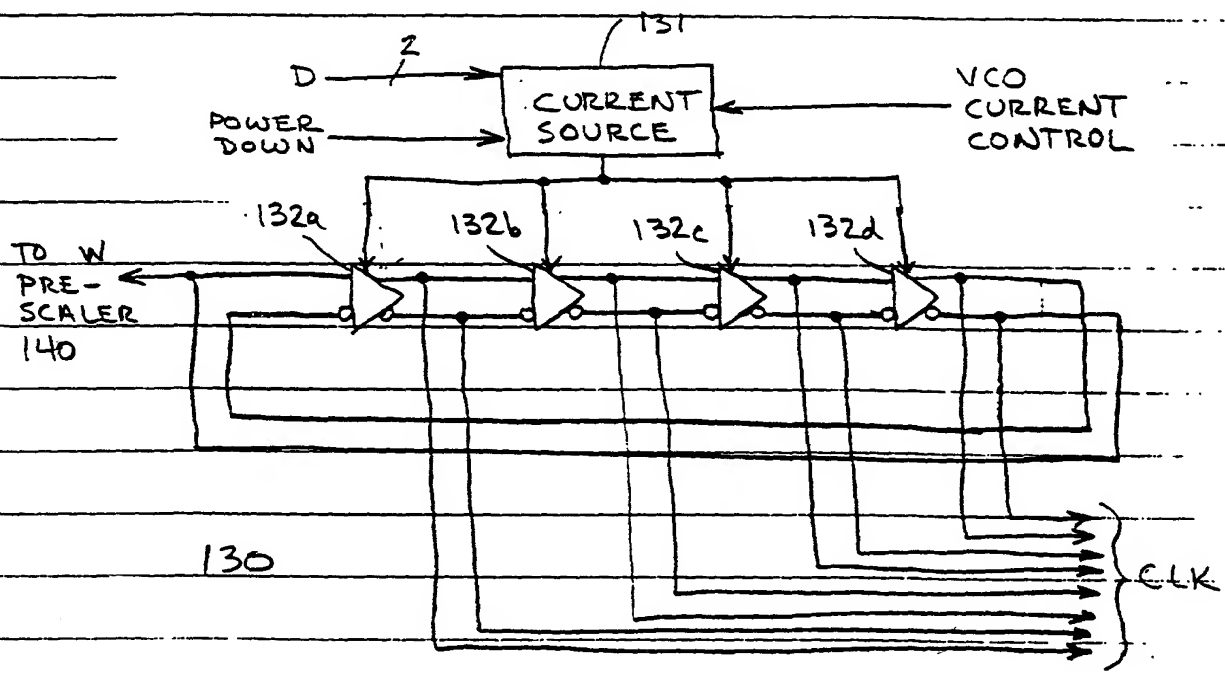


FIG. 3

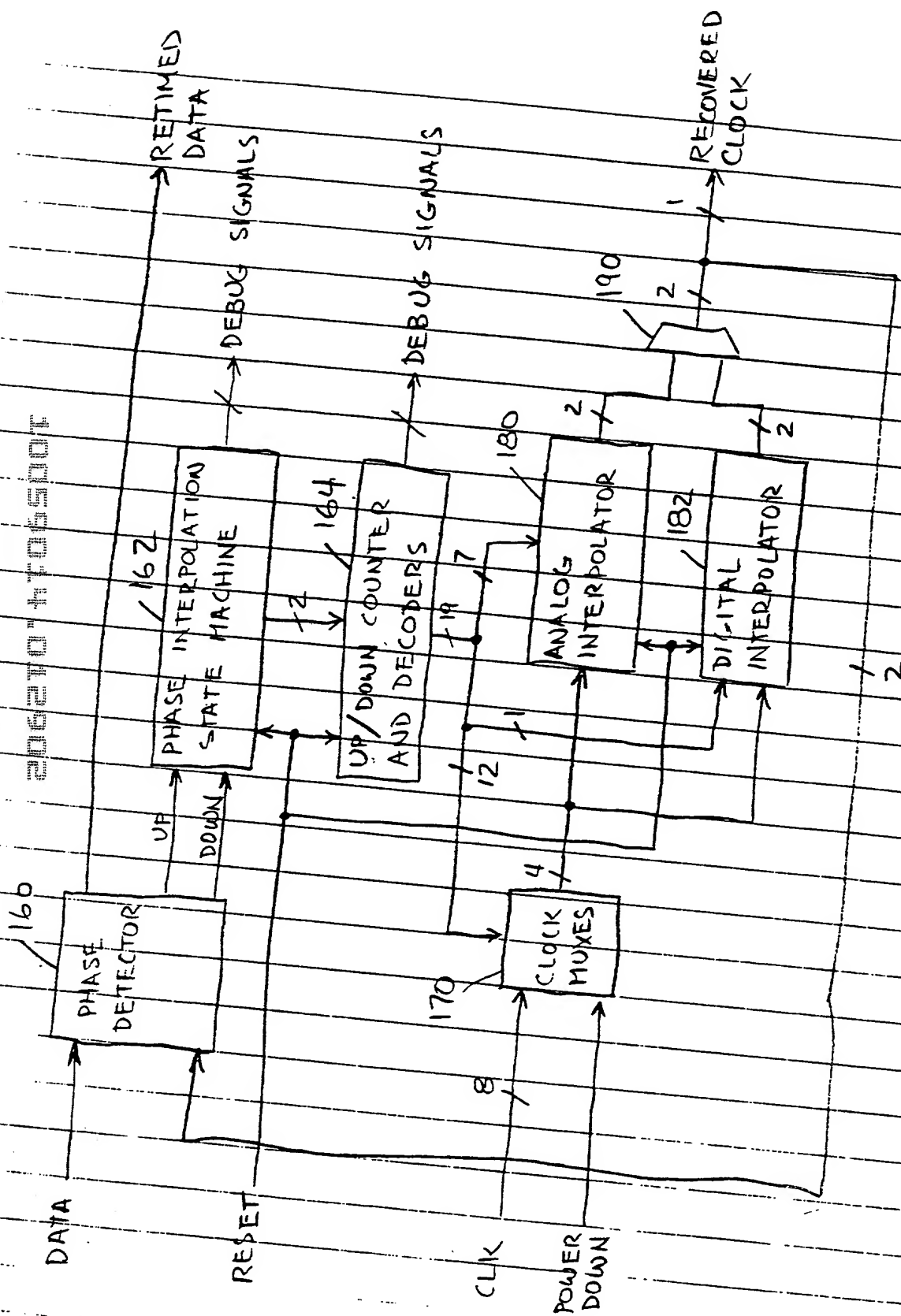


FIG. 4

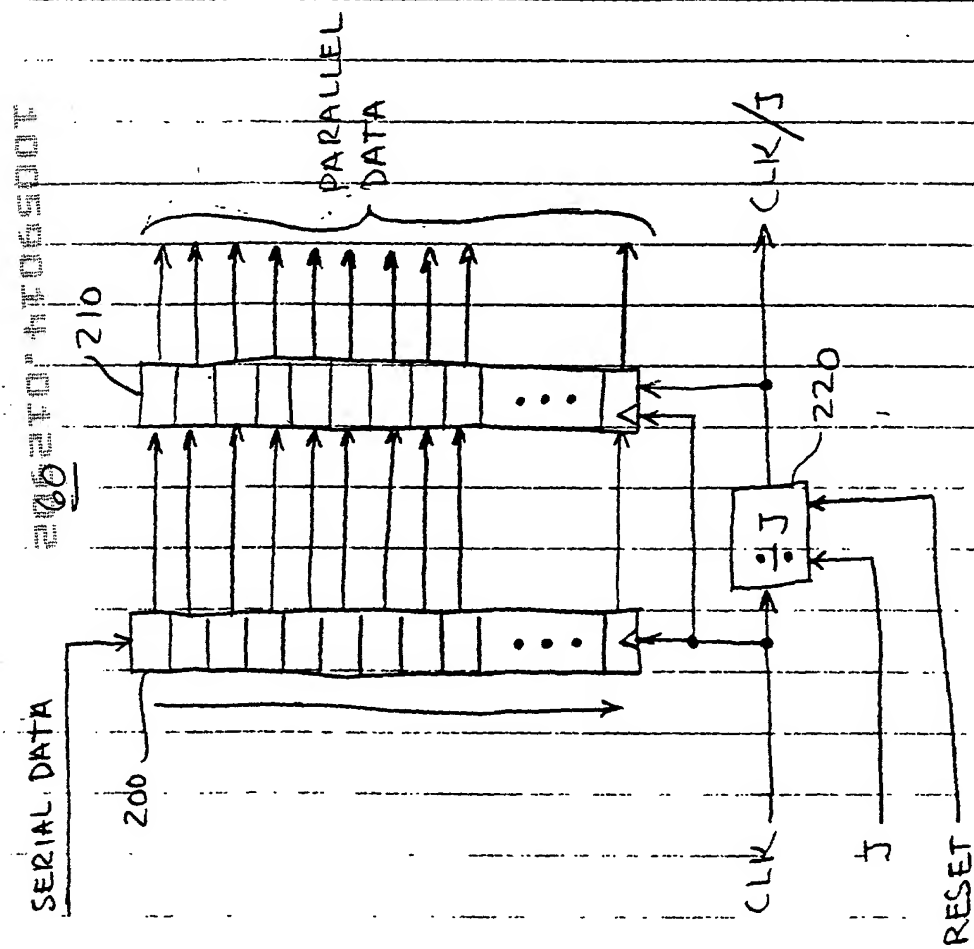


FIG. 5

70

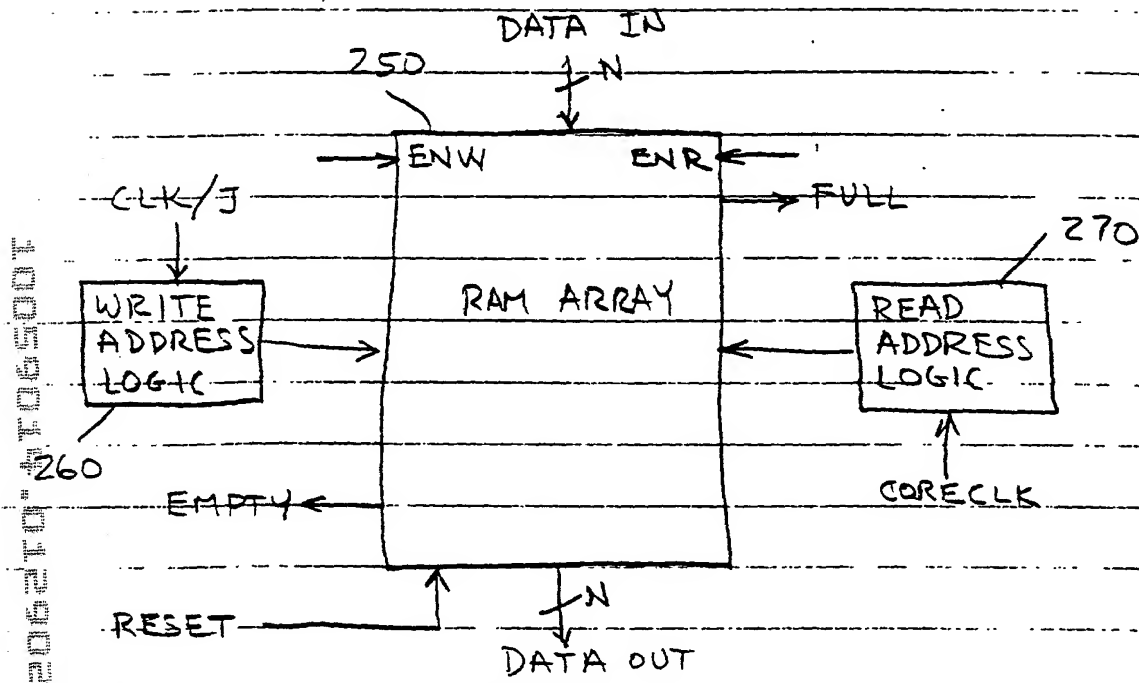


FIG. 6

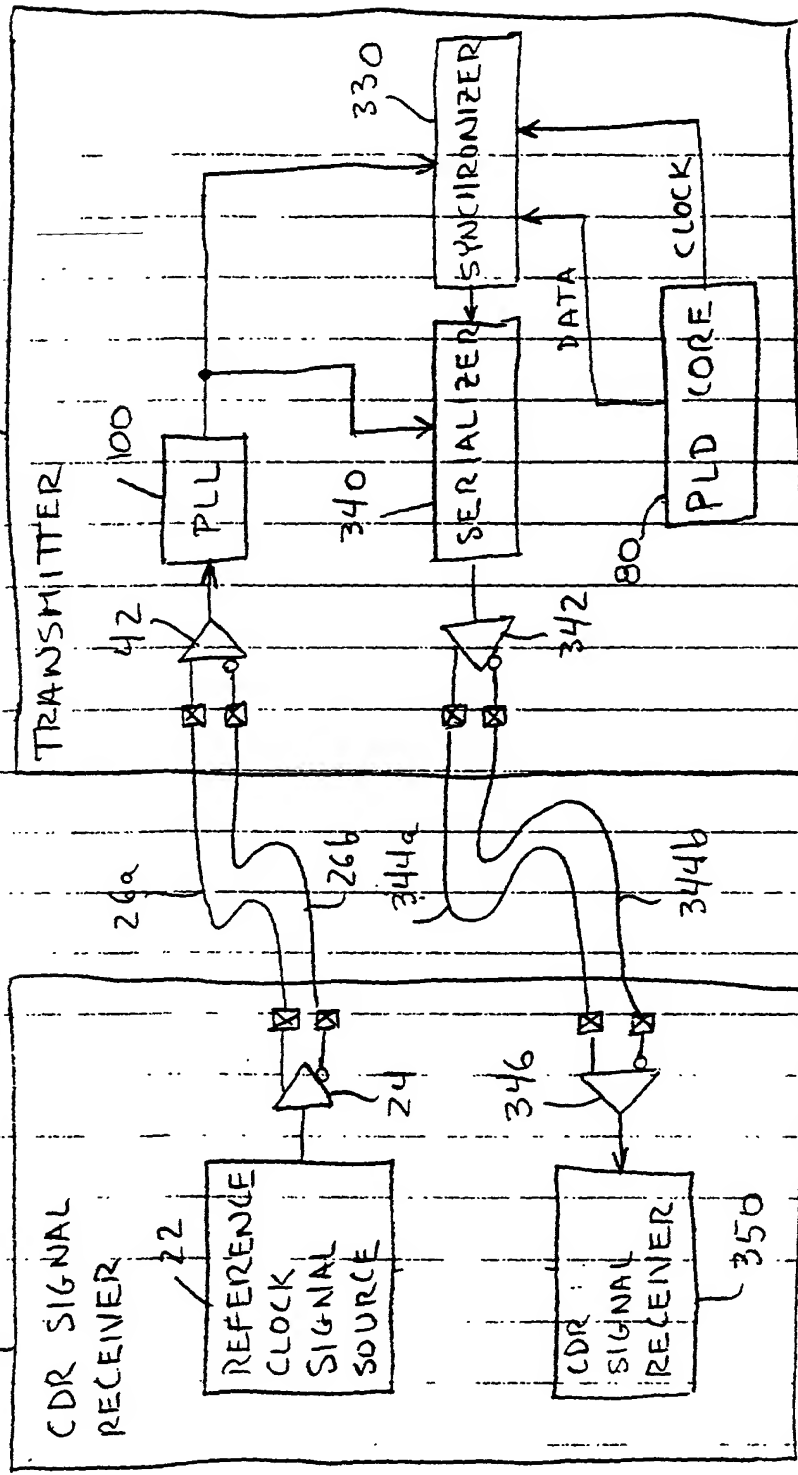


FIG. 7

20062011055001

300'

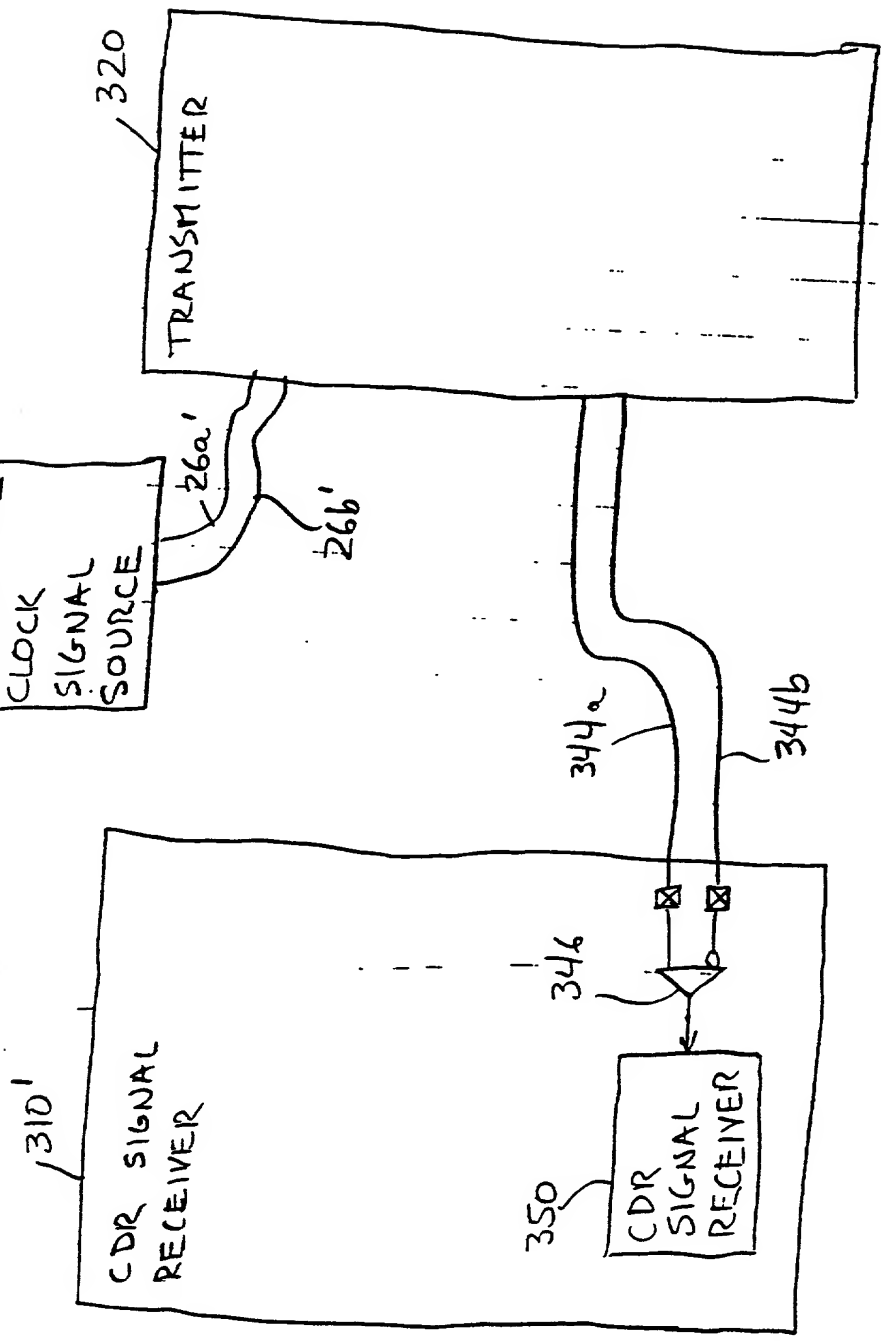


FIG. 7A

2062FO-4106500T 10059014-032900

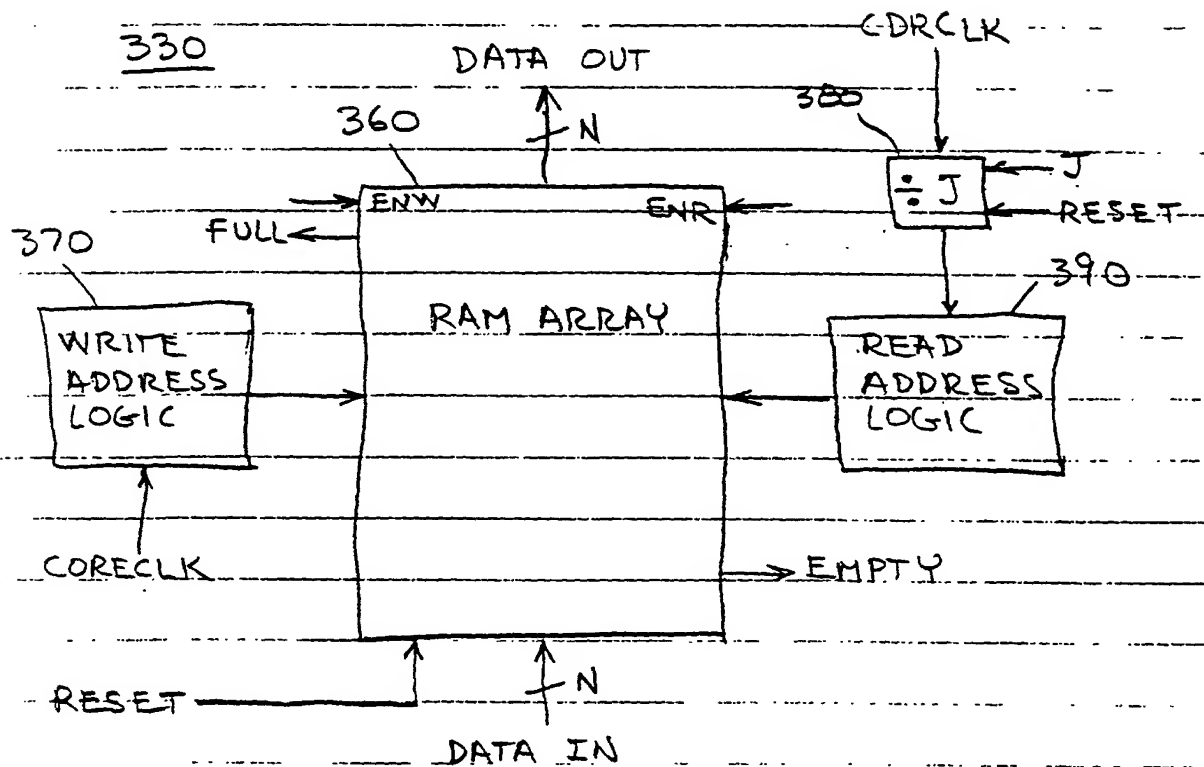


FIG. 8

340

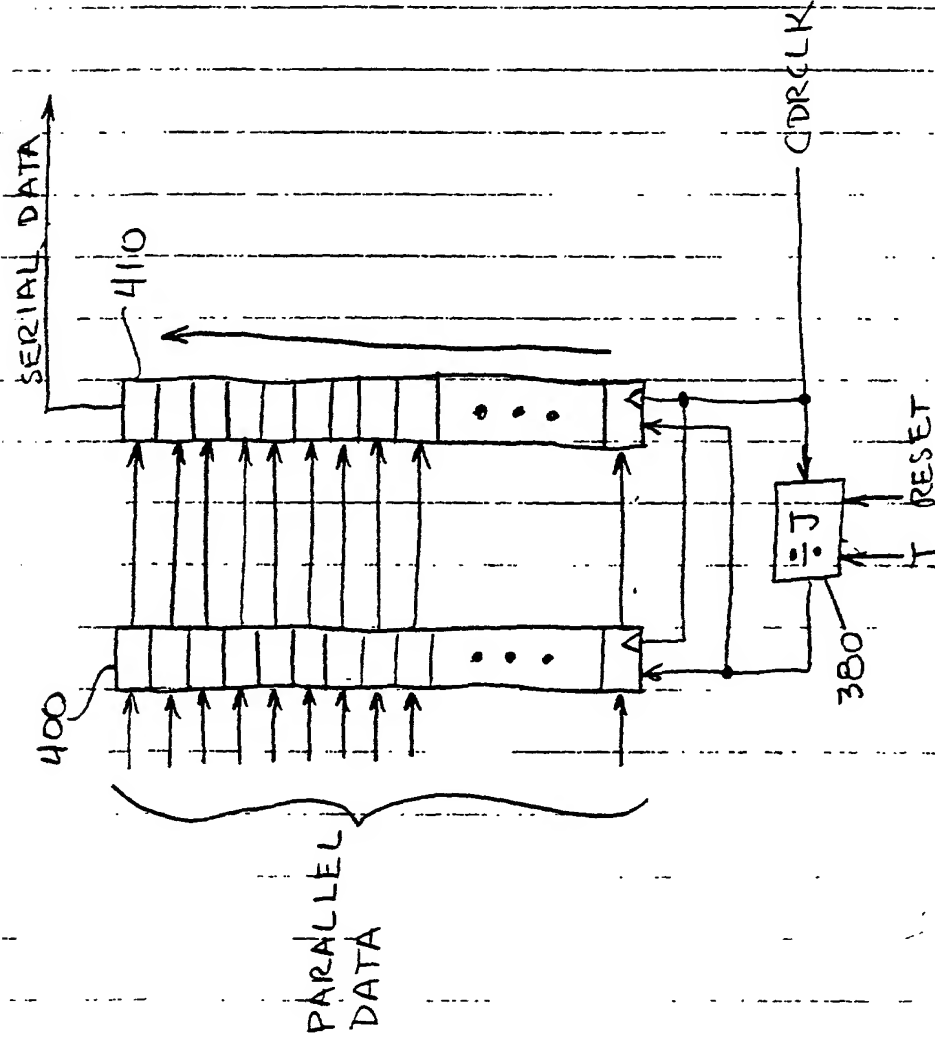


FIG. 9

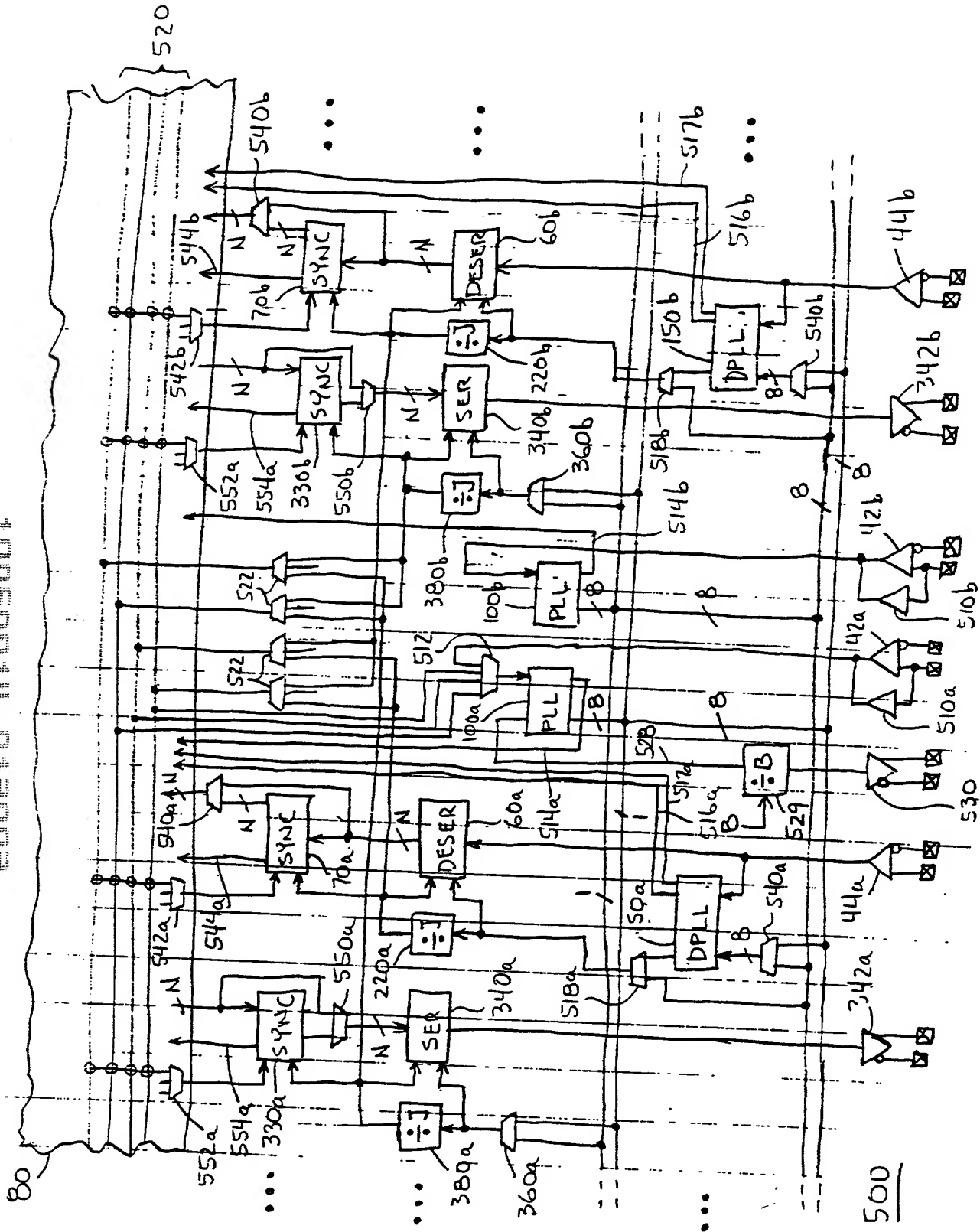


FIG. 10

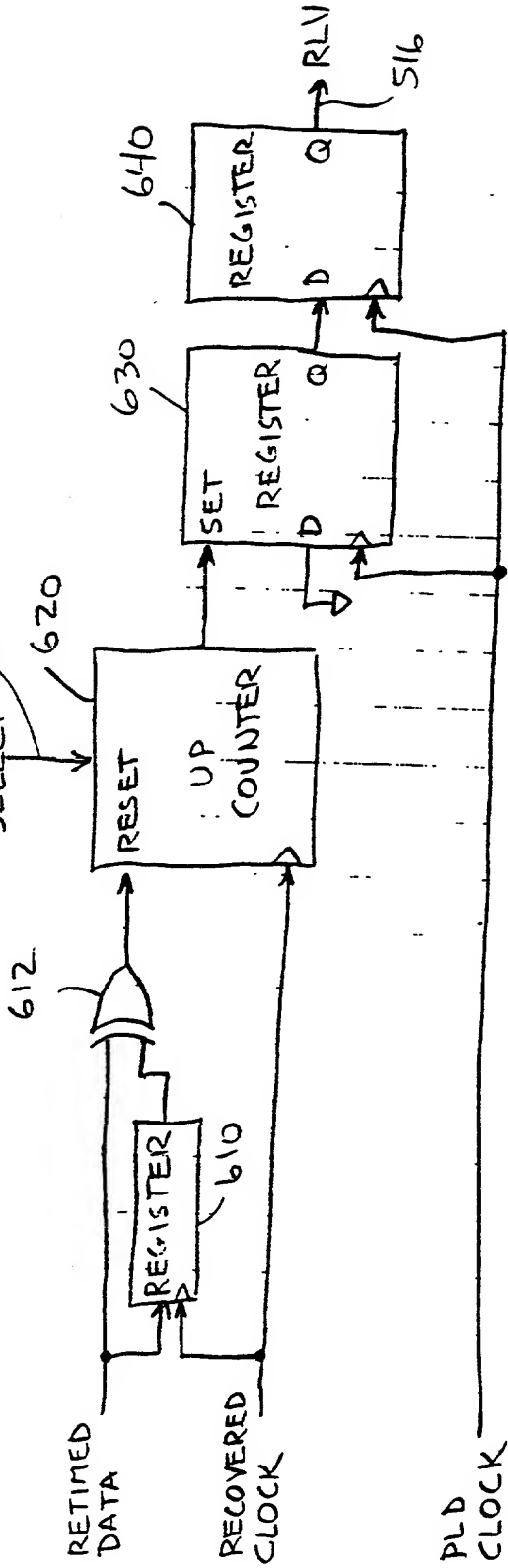


FIG. 10A

700

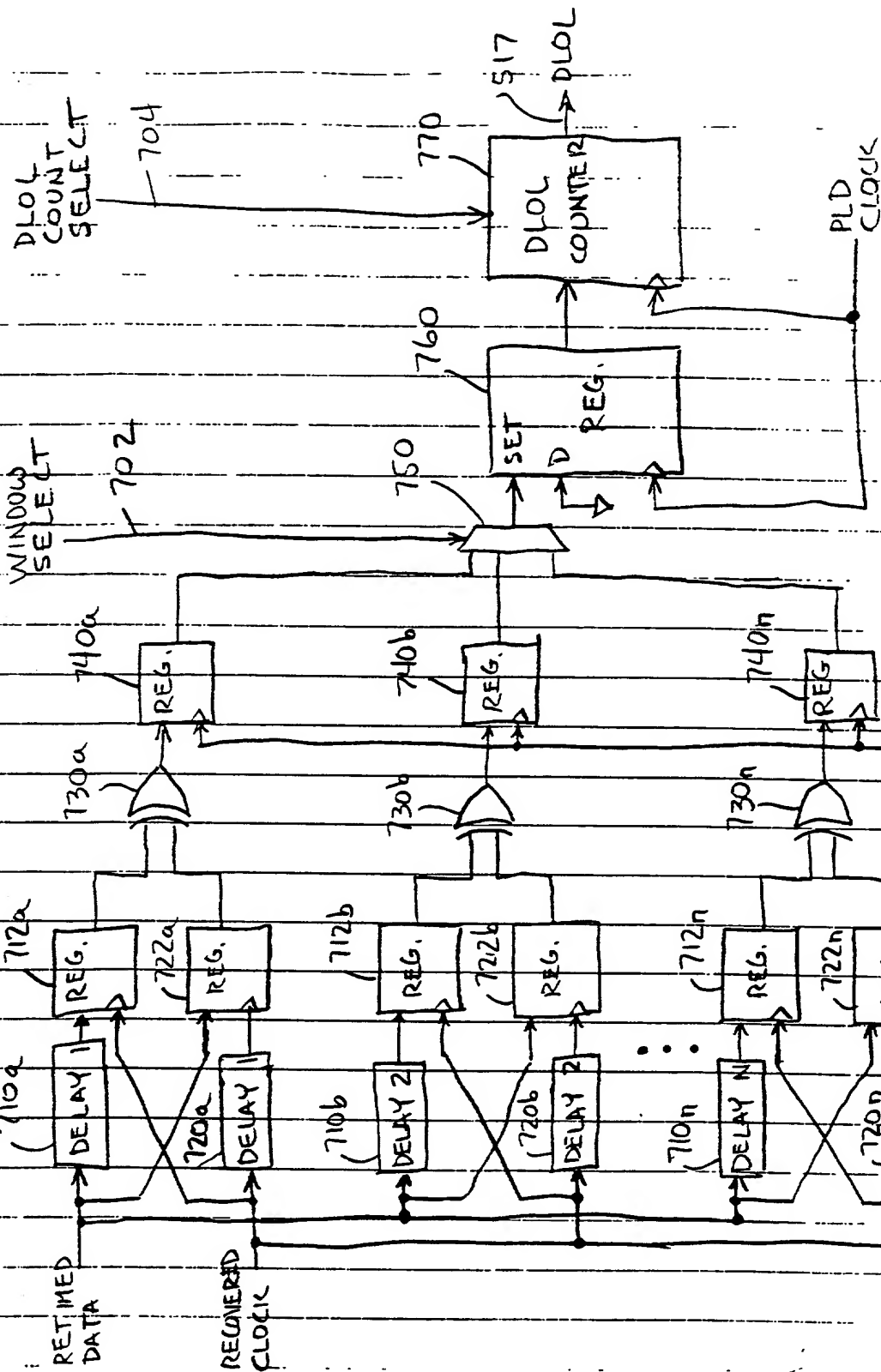


FIG. 10B

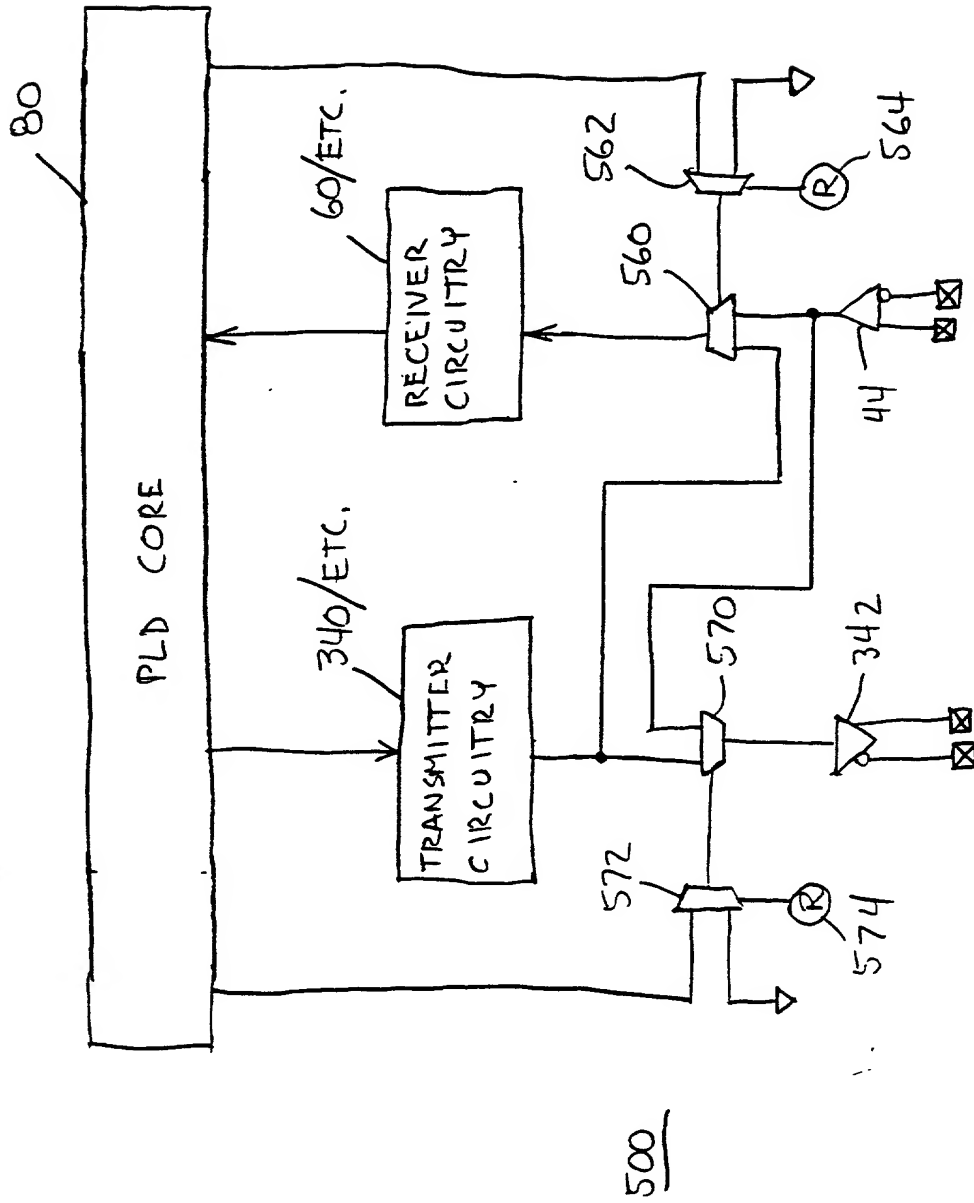


FIG. 10C

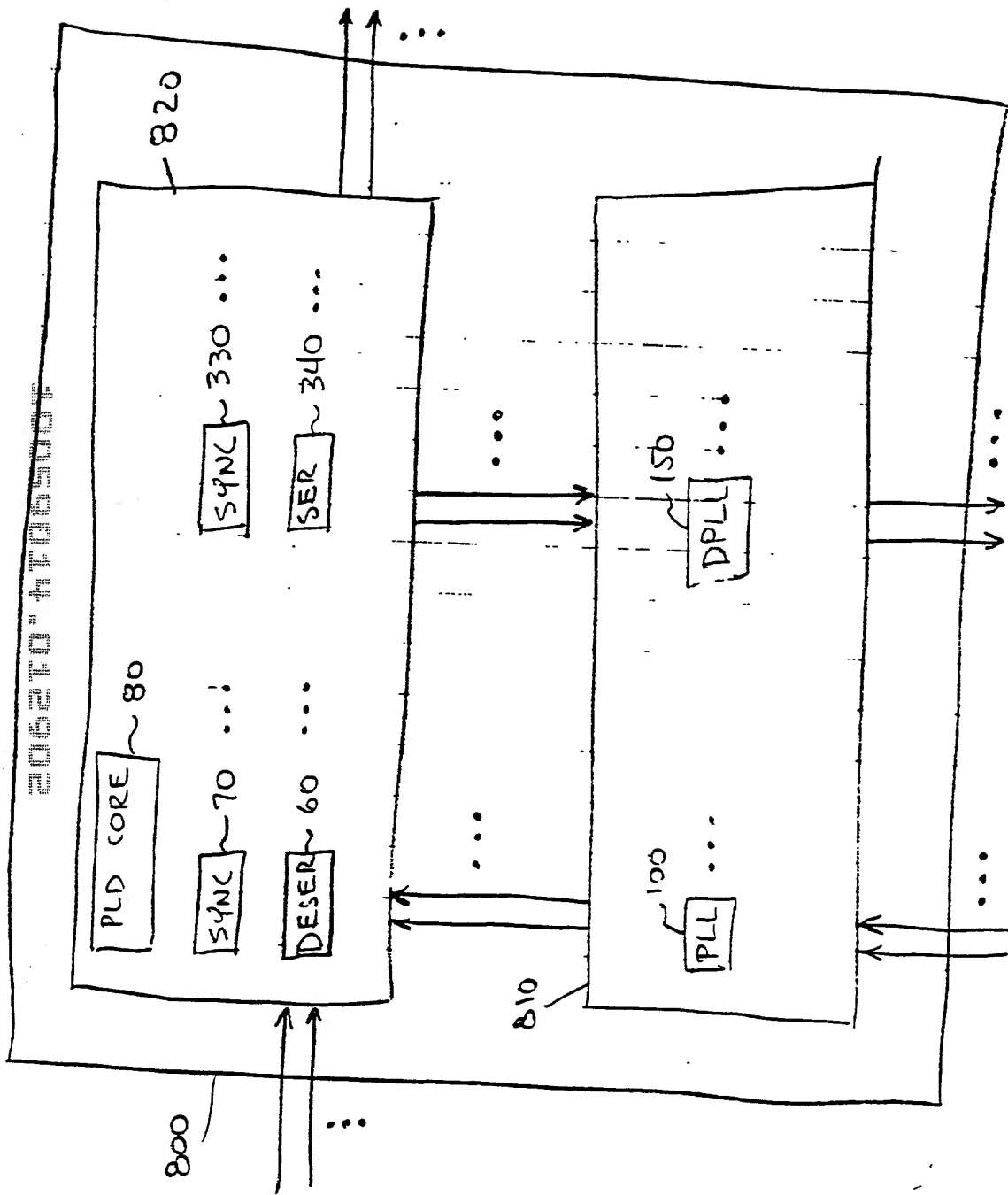
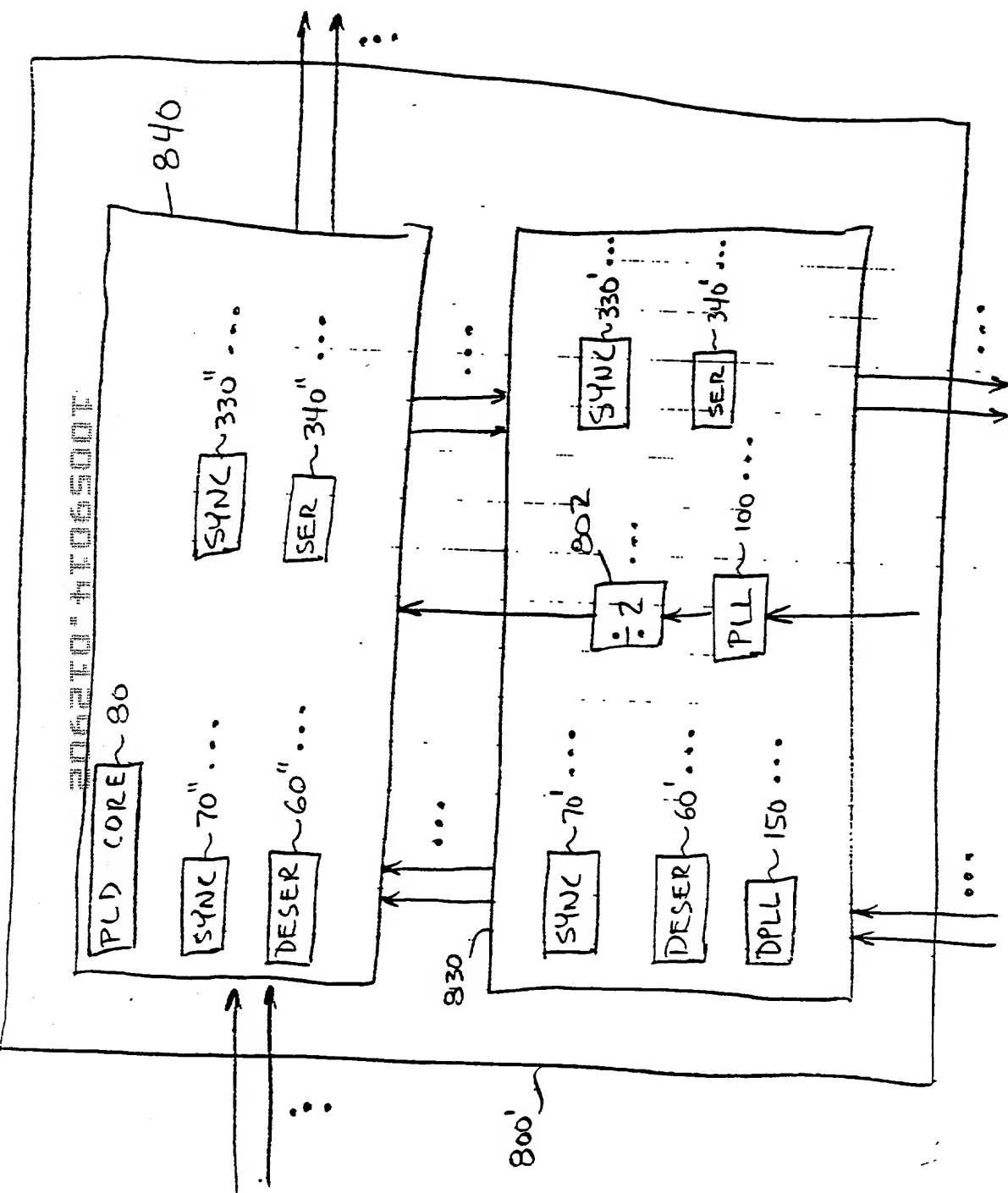


FIG. 11A



2062FO"4T06500T

800"

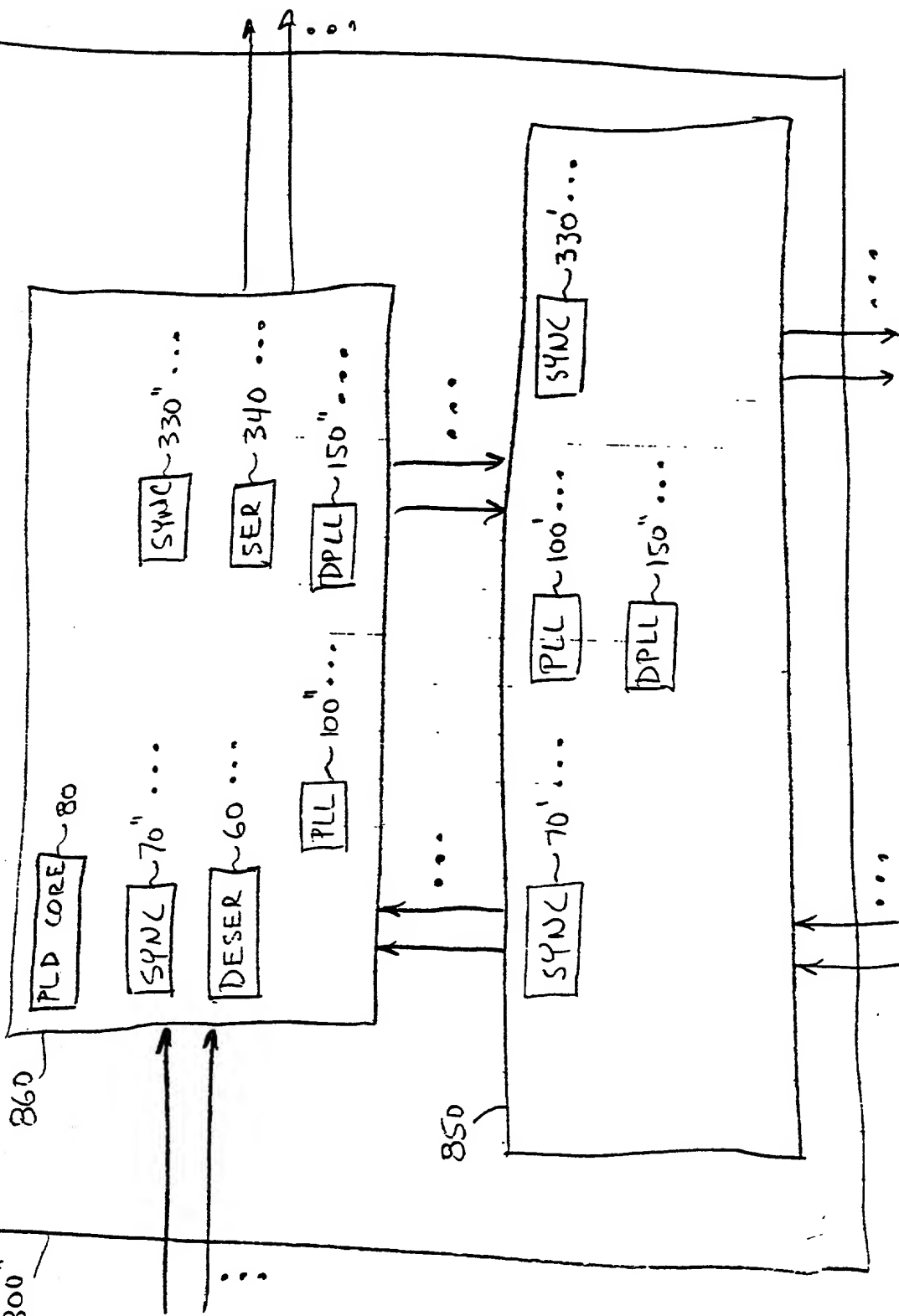
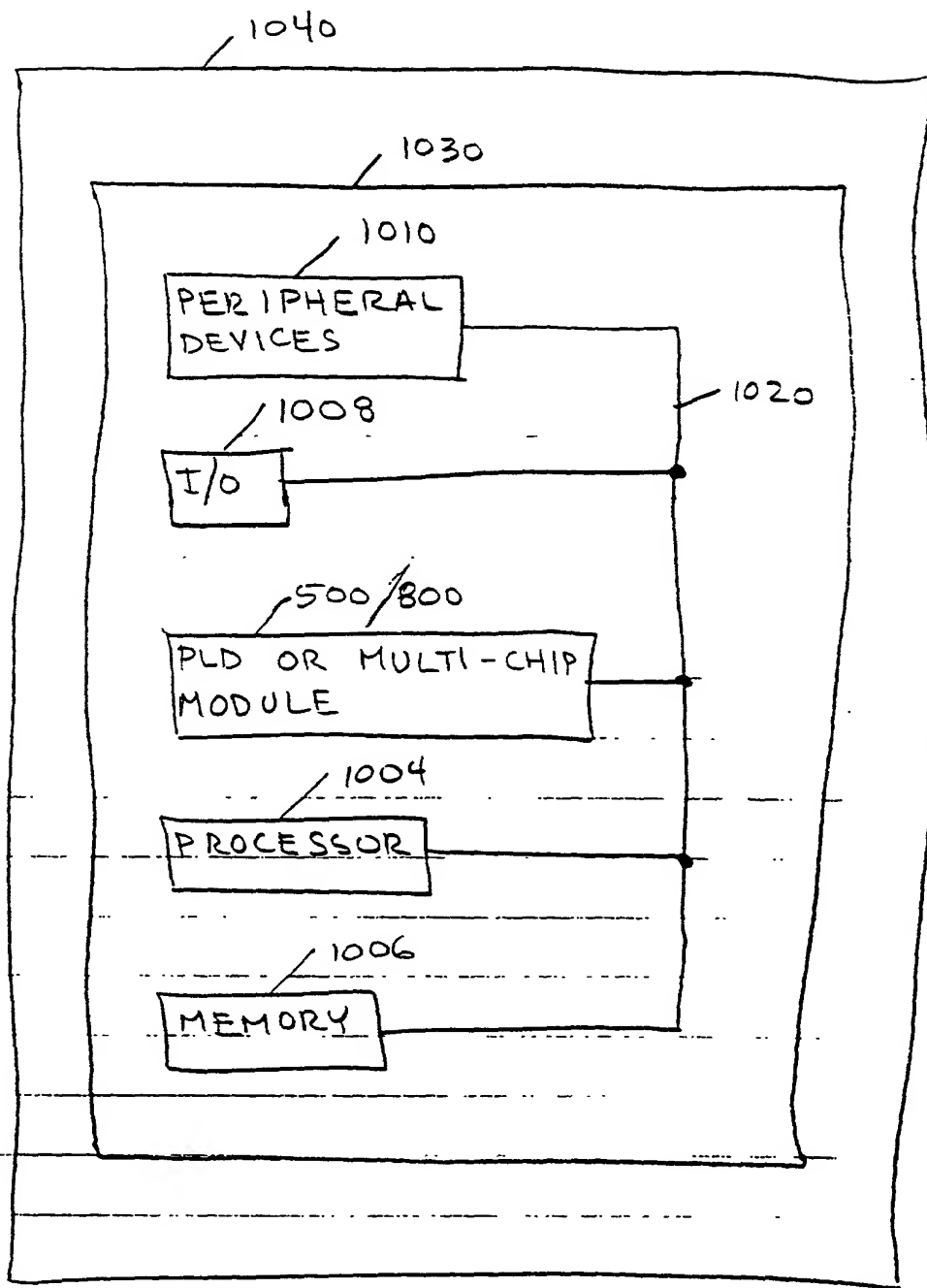


FIG. 11C

206270-4T065001



1002

FIG. 12

FIG. 13A

206270-1106500840

830

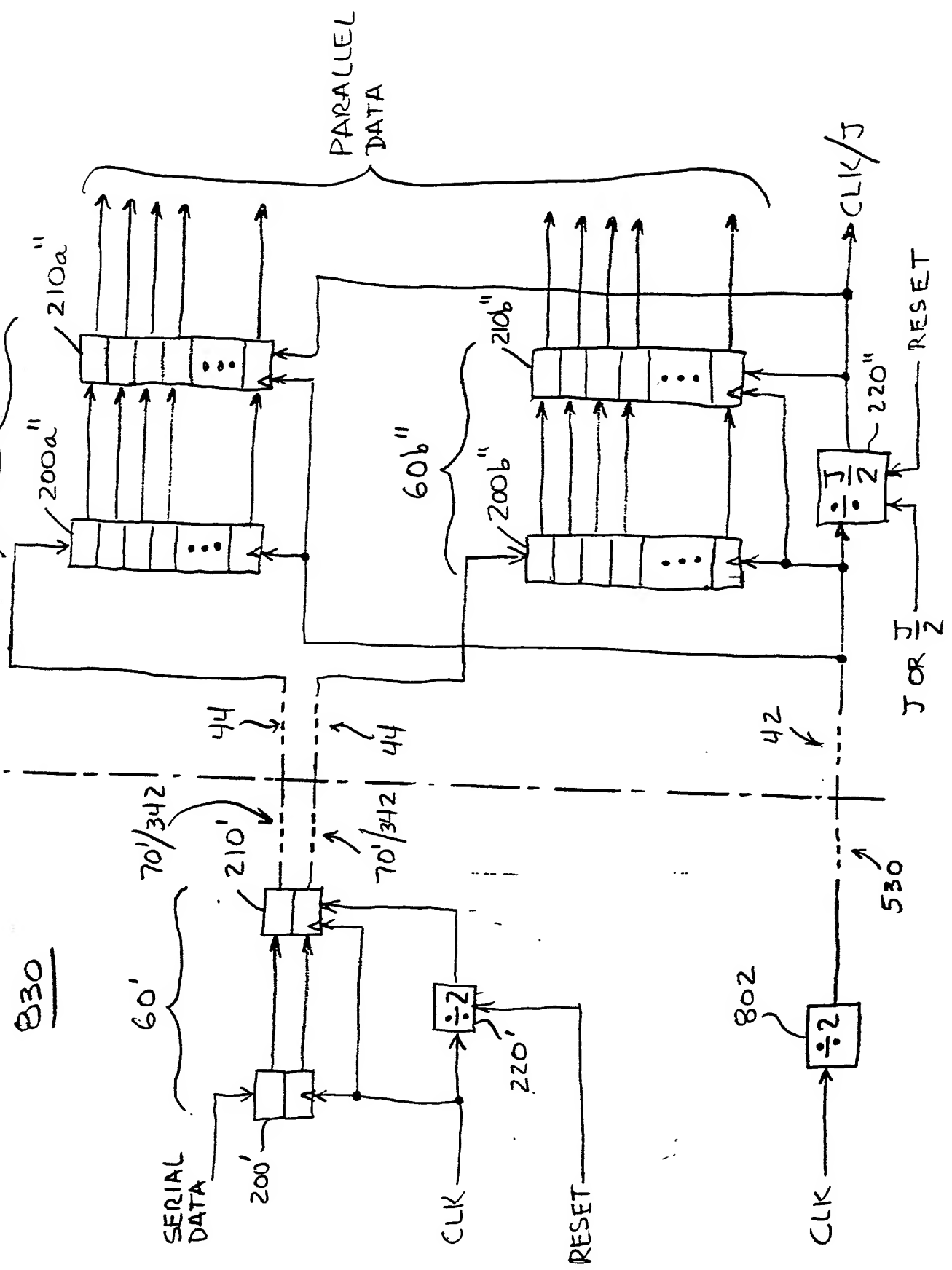
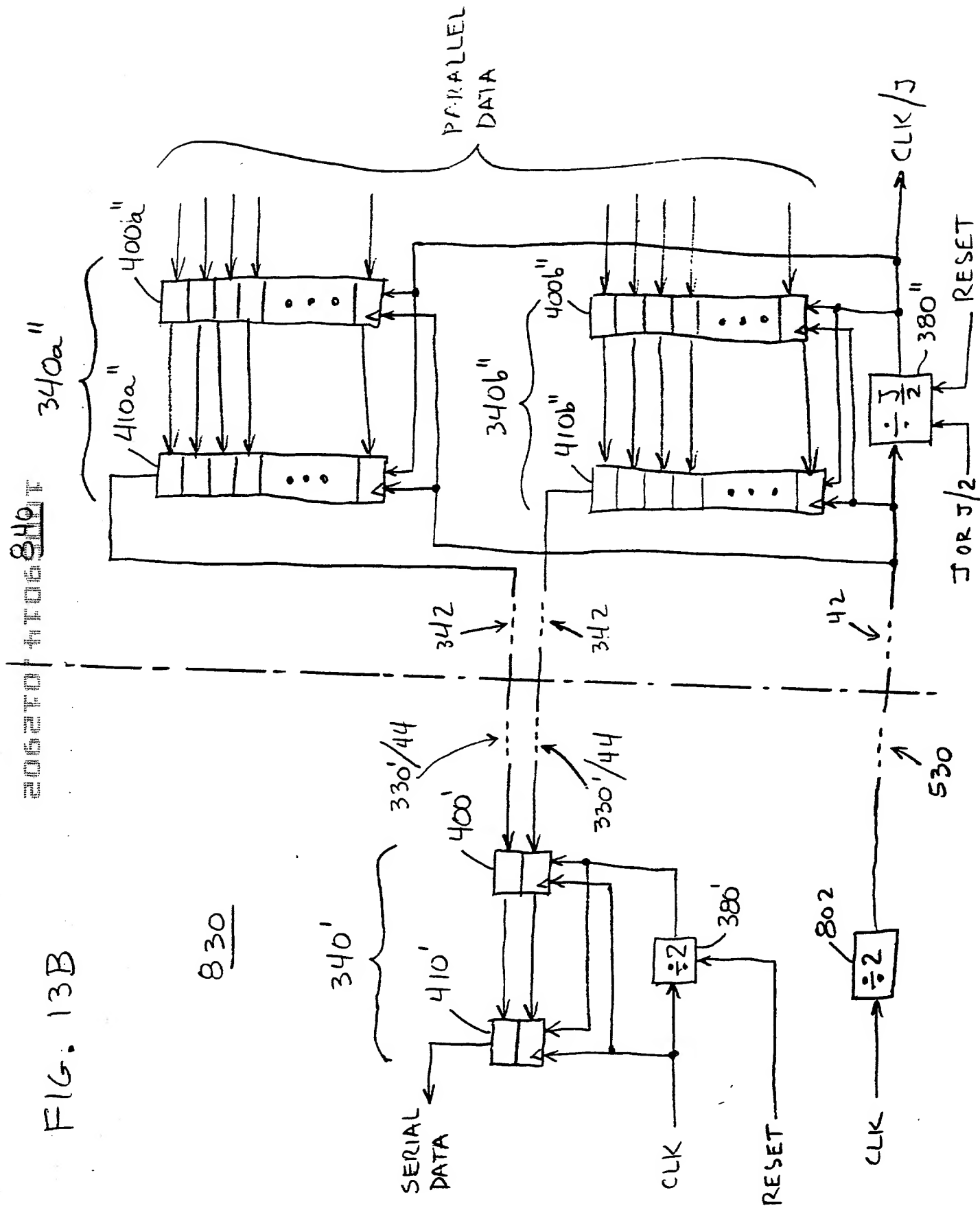


FIG. 13B



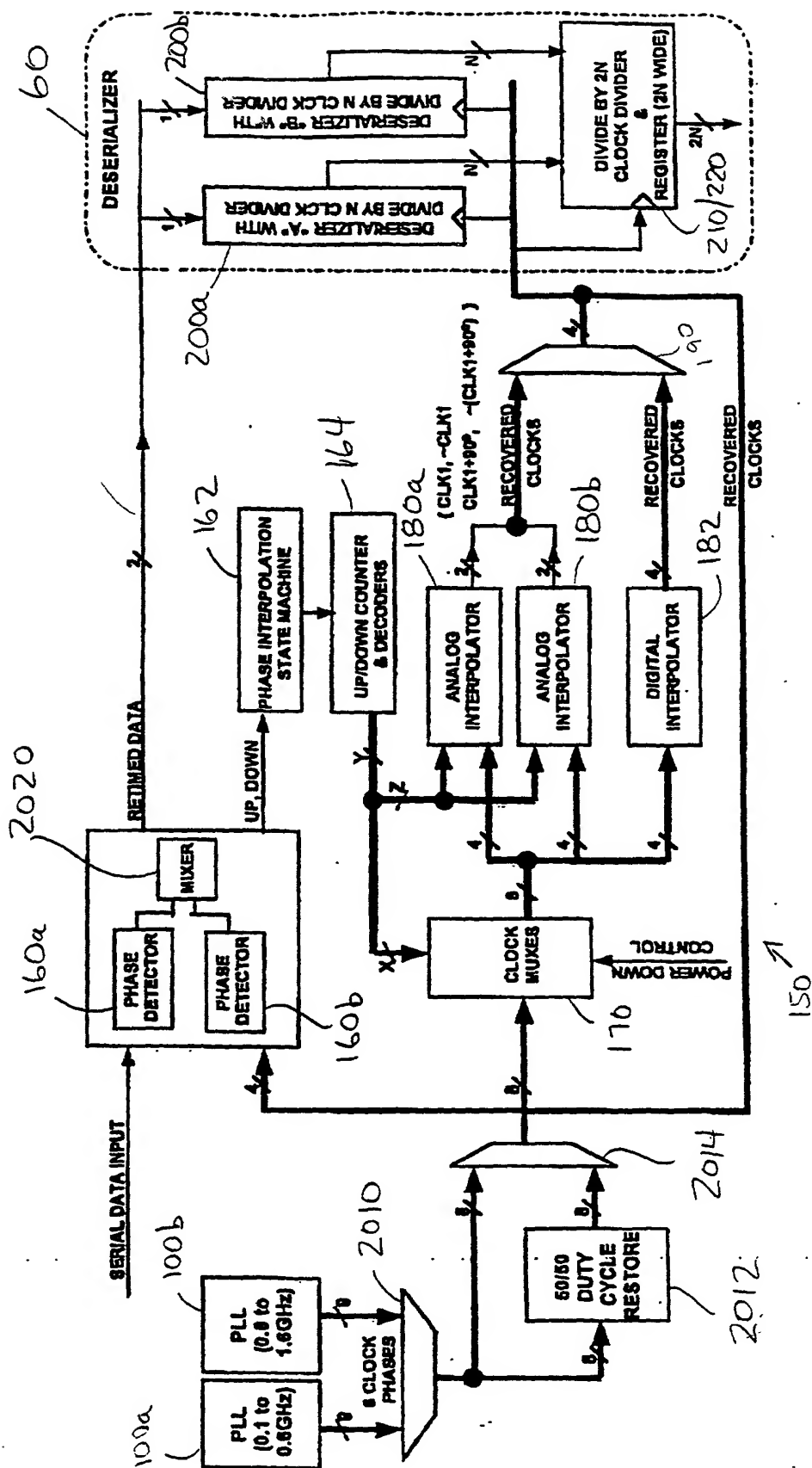


FIG. 14

206270-1065001

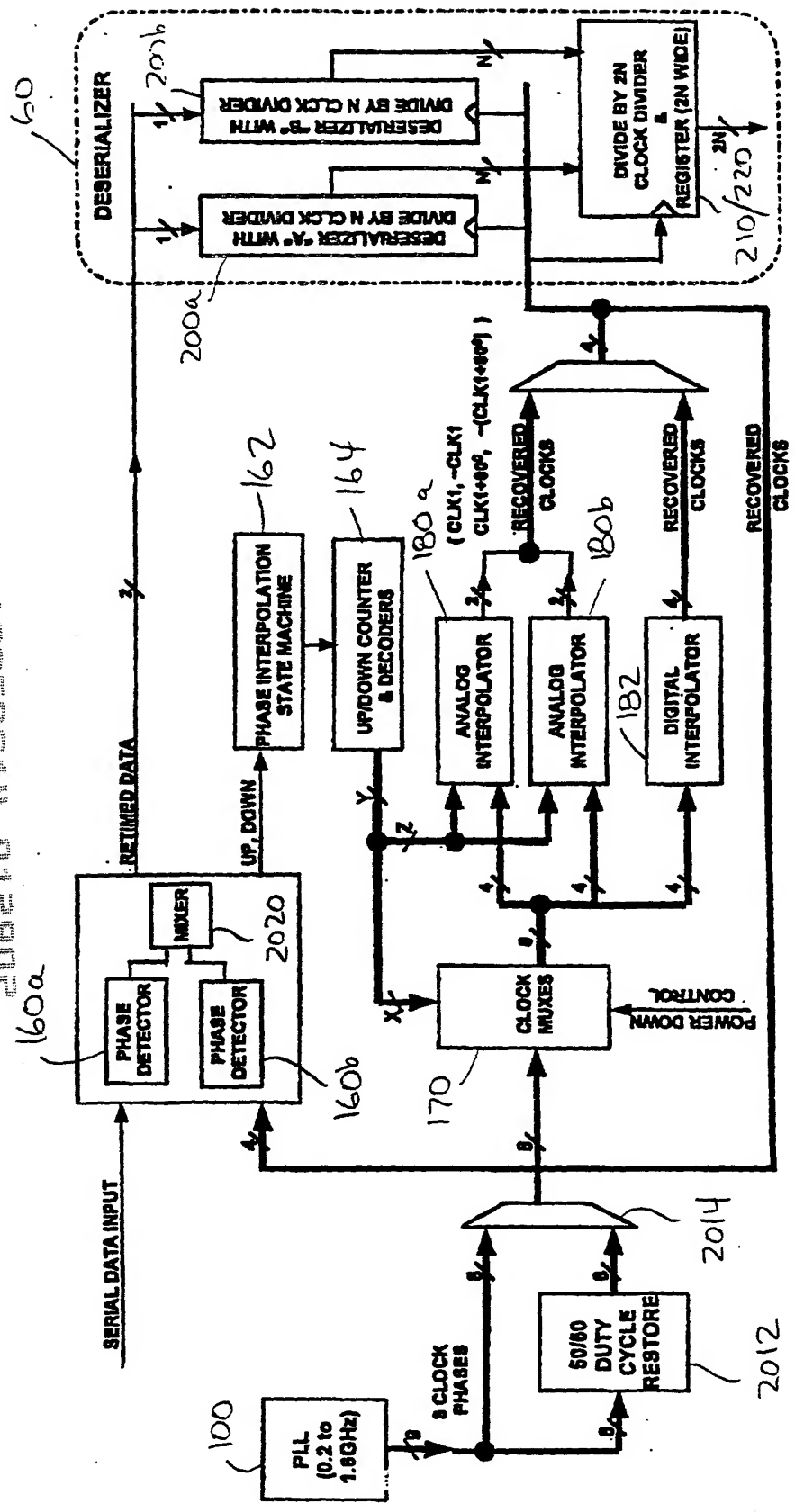


FIG. 15

CLK1 REGISTERS
BIT "N" OF DATA

CLK2 ALIGNS WITH
0-70-1 DATA EDGE

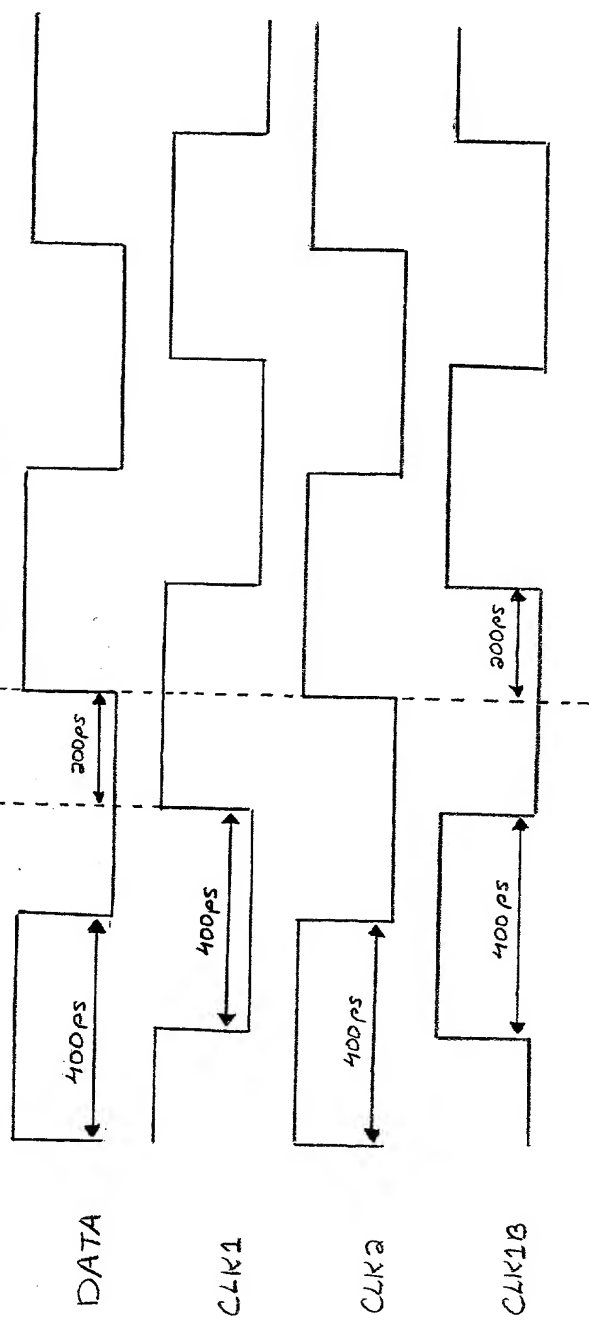


FIG. 16A

CLK1B REGISTERS
BIT "N+1" OF DATA

CLK2B ALIGNS WITH
1-70-0 DATA EDGE

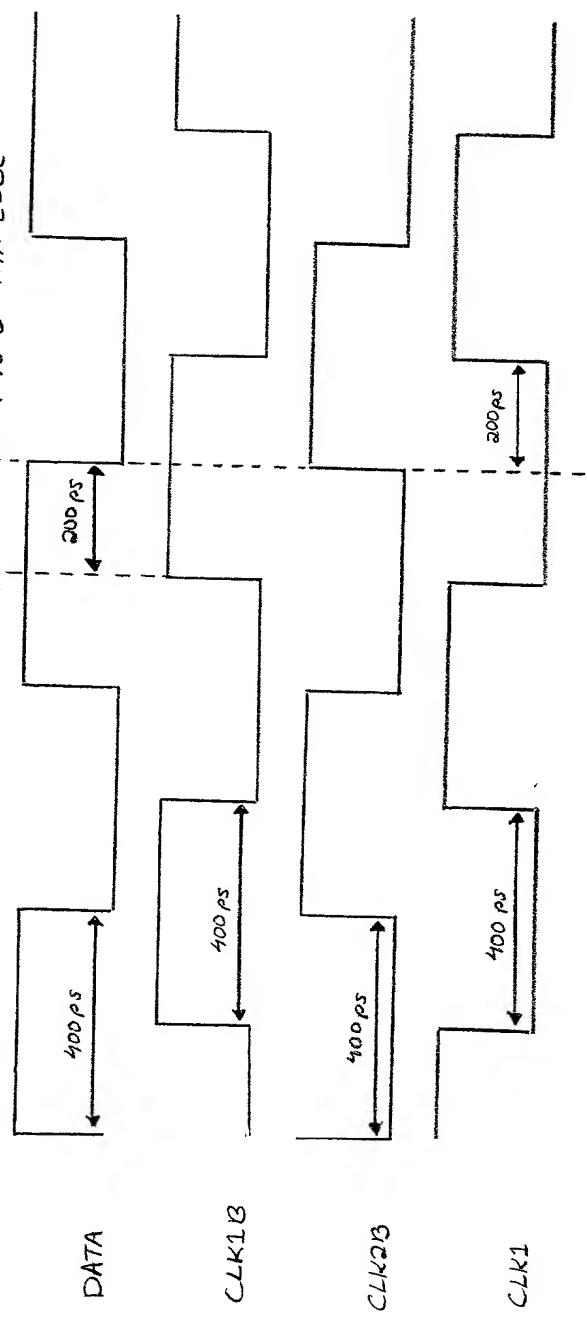


FIG. 16B

TIME

174 / 211

